

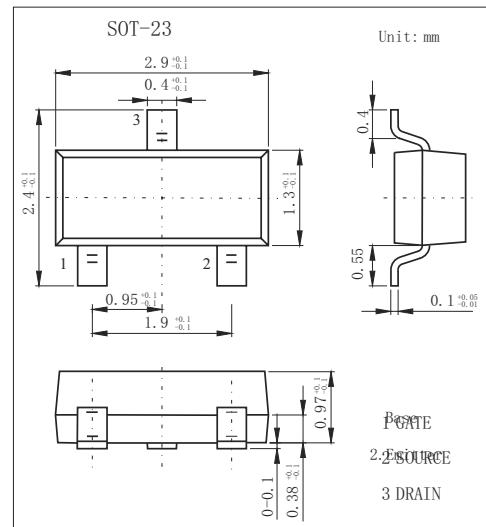
SOT-23 Plastic-Encapsulate MOSFETS

Features

- N-Channel Enhancement Mode Field Effect Transistor
- $V_{DS}=20V$, $R_{DS(ON)}=40m\Omega$ @ $V_{GS}=4.5V$, $I_D=5.0A$
- $V_{DS}=20V$, $R_{DS(ON)}=60m\Omega$ @ $V_{GS}=2.5V$, $I_D=4.0A$ $V_{DS}=20V$
- $R_{DS(ON)}=75m\Omega$ @ $V_{GS}=1.8V$, $I_D=1.0A$

MECHANICAL DATA

- Case style:SOT-23molded plastic
- Mounting position:any



MAXIMUM RATINGS AND CHARACTERISTICS

@ 25°C Ambient Temperature (unless otherwise noted)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	20	V
Gate-Source Voltage	V_{GS}	± 10	V
Drain-Current -Continuous * $T_J=125^{\circ}\text{C}$ -Pulsed	I_D	3.8	A
	I_{DM}	15	A
Power Dissipation *	P_D	1.25	W
Thermal Resistance,Junction- to-Ambient	R_{thJA}	100	$^{\circ}\text{C}/\text{W}$
Operating Junction and Storage Temperature Range	$T_{j,Tstg}$	-55 to 150	$^{\circ}\text{C}$

* Surface Mounted on FR 4 Board , $t \leq 10$ sec.

MOSFET ELECTRICAL CHARACTERISTICS $T_a=25^{\circ}\text{C}$ unless otherwise specified

Parameter	Symbol	Testconditons	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	V_{DSS}	$V_{GS}=0V, I_D=250\mu\text{A}$	20			V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=20V, V_{GS}=0V$			1	μA
Gate-Body Leakage	I_{GSS}	$V_{GS}=\pm 10V, V_{DS}=0V$			± 100	nA
Gate Threshold Voltage *	$V_{GS(th)}$	$V_{GS}=V_{DS}, I_D=250\mu\text{A}$	0.6	0.78	1.5	V
Drain- Source on-state Resistance *	$R_{DS(ON)}$	$V_{GS}=4.5V, I_D=5.0A$		32	40	$\text{m}\Omega$
		$V_{GS}=2.5V, I_D=4.0A$		50	60	$\text{m}\Omega$
		$V_{GS}=1.8V, I_D=1.0A$		62	75	$\text{m}\Omega$
On-State Drain Current *	$I_{D(ON)}$	$V_{DS}=5V, V_{GS}=4.5V$	18			A
Forward Transconductance *	g_{FS}	$V_{DS}=5V, I_D=5A$	5			S
Input Capacitance	C_{iss}	$V_{DS} = 15V, V_{GS} = 0V, f = 1.0\text{MHz}$		888		pF
Output Capacitance	C_{oss}			144		pF
Reverse Transfer Capacitance	C_{rss}			115		pF
Turn-On Delay Time	$t_{D(on)}$	$V_{DD}=10V, I_D=1A, V_{GS}=4.5V, R_L=10\Omega, R_{GEN}=6\Omega$		31.8		ns
Rise Time	t_r			14.5		ns
Turn-Off Delay Time	$t_{D(off)}$			50.3		ns
Fall Time	t_f			31.9		ns
Total Gate Charge	Q_g	$V_{DS} = 10V, I_D = 3.5A, V_{GS} = 4.5V$		16.8		nC
Gate-S ource Charge	Q_{gs}			2.5		nC
Gate-Drain Charge	Q_{gd}			5.4		nC
Drain-Source Diode Forward Current *	I_S				1.25	A
Diode Forward Voltage	V_{SD}	$V_{GS}=0V, I_S=1.25A$		0.825	1.2	V

* Pulse Test:Pulse Width $\leq 300\mu\text{s}$,Duty Cycle $\leq 2\%$